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Dallas, Texas 75244**MAR 07 2007**Main No. (972) 385-8777  
Facsimile (972) 385-7766**FACSIMILE COVER SHEET**

To: Commissioner for Patents for <b>Examiner Carlos David Amaya</b> <b>Group Art Unit 2836</b>	Facsimile No. <b>571/273-8300</b>
From: Joyce K. Stafford <b>Legal Assistant to Rakesh Garg</b>	No. of Pages Including Cover Sheet: <b>8</b>
<p>Enclosed herewith:</p> <ul style="list-style-type: none"> <li>• Transmittal; and</li> <li>• Response to Final Office Action.</li> </ul>	
<p>Rc: Application Serial No. 09/938,081 Attorney Docket No. 01-840</p>	
<p>Date: Wednesday, March 7, 2007</p>	
<p><b>Please contact us at (972) 385-8777 if you do not receive all pages indicated above or experience any difficulty in receiving this facsimile.</b></p>	<p><i>This Facsimile is intended only for the use of the addressee and, if the addressee is a client or their agent, contains privileged and confidential information. If you are not the intended recipient of this facsimile, you have received this facsimile inadvertently and in error. Any review, dissemination, distribution, or copying is strictly prohibited. If you received this facsimile in error, please notify us by telephone and return the facsimile to us immediately.</i></p>

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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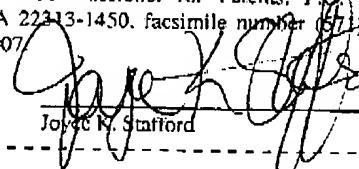
MAR 07 2007

In re application of: **Jablonski et al.**

§ Group Art Unit: 2836

Serial No.: **09/938,081**§ Examiner: **Amaya, Carlos David**Filed: **August 23, 2001**§ Attorney Docket No.: **01-840**For: **Interface Circuit for Providing a Computer Logic Circuit with First and Second Voltages and an Associated Method****24319**PATENT TRADEMARK OFFICE  
CUSTOMER NUMBER

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**Certificate of Transmission Under 37 C.F.R. § 1.8(a)**  
 I hereby certify this correspondence is being transmitted via facsimile to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, facsimile number (703) 273-8300 on March 7, 2007.

By: 

Joyce N. Stafford

TRANSMITTAL

Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

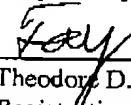
Sir:

ENCLOSED HEREWITH:

- Response to Final Office Action

No fees are believed to be required. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to LSI Logic Corporation Deposit Account No. 12-2252. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to LSI Logic Corporation Deposit Account No. 12-2252.

Respectfully submitted,

  
 Theodore D. Fay III  
 Registration No. 48,504

Duke W. Yee  
 Registration No. 34,285  
 YEE & ASSOCIATES, P.C.  
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 ATTORNEYS FOR APPLICANTS

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAR 07 2007

In re application of: **Jablonski et al.**

Serial No.: **09/938,081**

Filed: **August 23, 2001**

For: **Interface Circuit for Providing a Computer Logic Circuit with First and Second Voltages and an Associated Method**

§ Group Art Unit: **2836**

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§ Examiner: **Amaya, Carlos David**

§

§ Attorney Docket No.: **01-840**

§

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By:

Joyce K. Stafford

24319

PATENT TRADEMARK OFFICE  
CUSTOMER NUMBER

Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

**RESPONSE TO FINAL OFFICE ACTION**

Sir:

No fees are believed to be required. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to LSI Logic Corporation Deposit Account No. 12-2252. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to LSI Logic Corporation Deposit Account No. 12-2252.

In response to the Final Office Action of December 7, 2006, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims, which begins on page 2 of this paper. Remarks/Arguments begin on page 5 of this paper.